

A/C3
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a data line provided on said substrate;

a wiring provided on said substrate;

a pixel electrode provided on said substrate and superposed on said gate line with a first insulator therebetween and superposed on said wiring with a second insulator therebetween; and

at least one transistor provided on said substrate and connected with said gate line at a gate thereof and connected with said data line at one of source and drain thereof and connected with said pixel electrode at the other one of the source and drain,

wherein said first insulator comprises an anodic oxide of a material of said gate line, and said second insulator comprises an anodic oxide of a material of said wiring.--

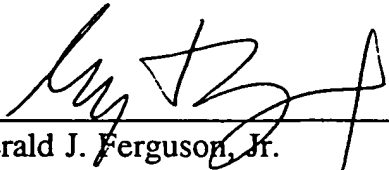
REMARKS

In response to the election requirement in the Office Action of October 26, 1994 applicants hereby elect without traverse the Group I claims - that is, claims 1-8, drawn to an active matrix display device. Moreover, species A is also elected where claim 5 reads on species A.

New claims 21-23 have been added to complete the scope of applicant's protection.

Examination on the merits is now requested.

Respectfully submitted,



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